

FIG. 9

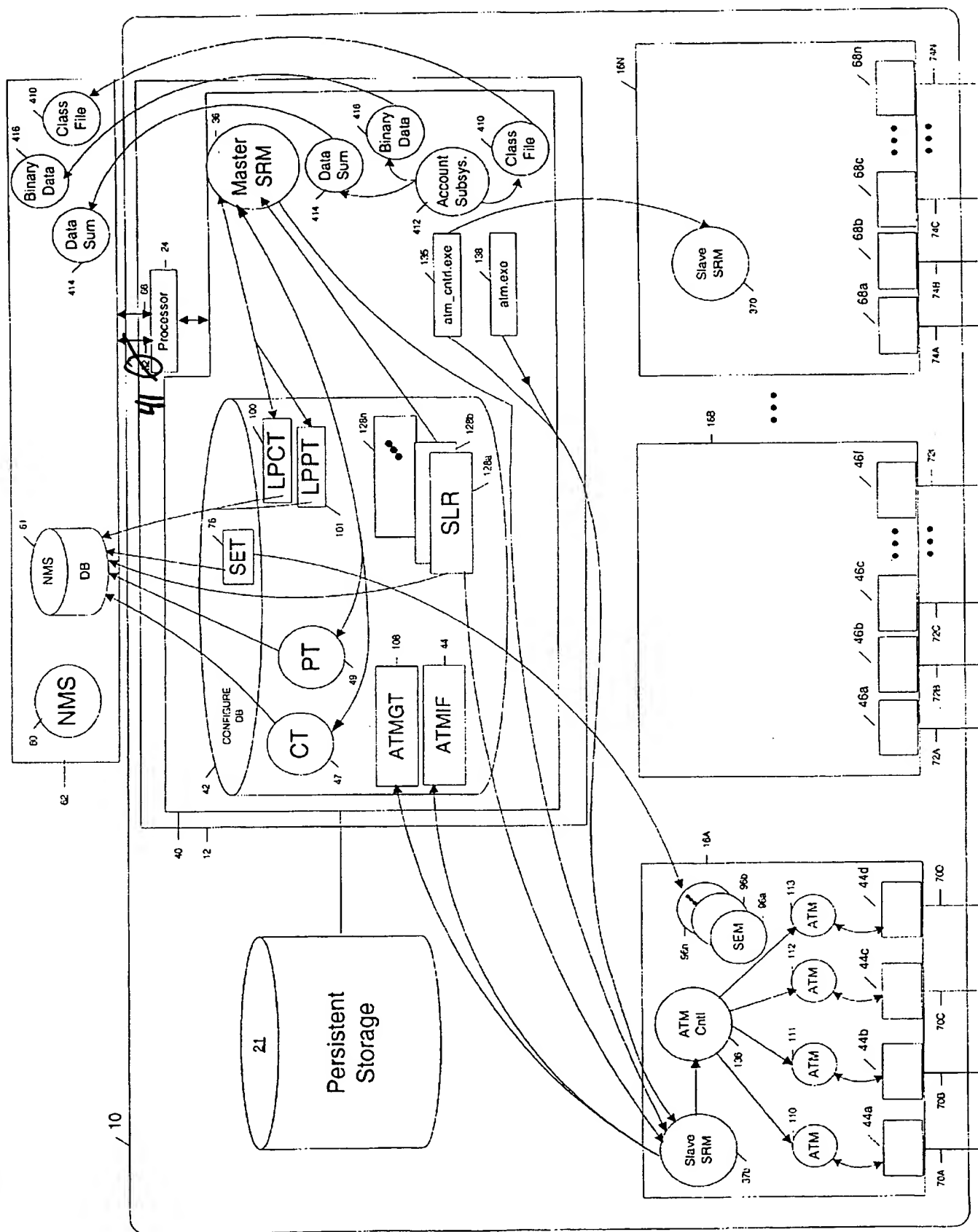


FIG. 16c

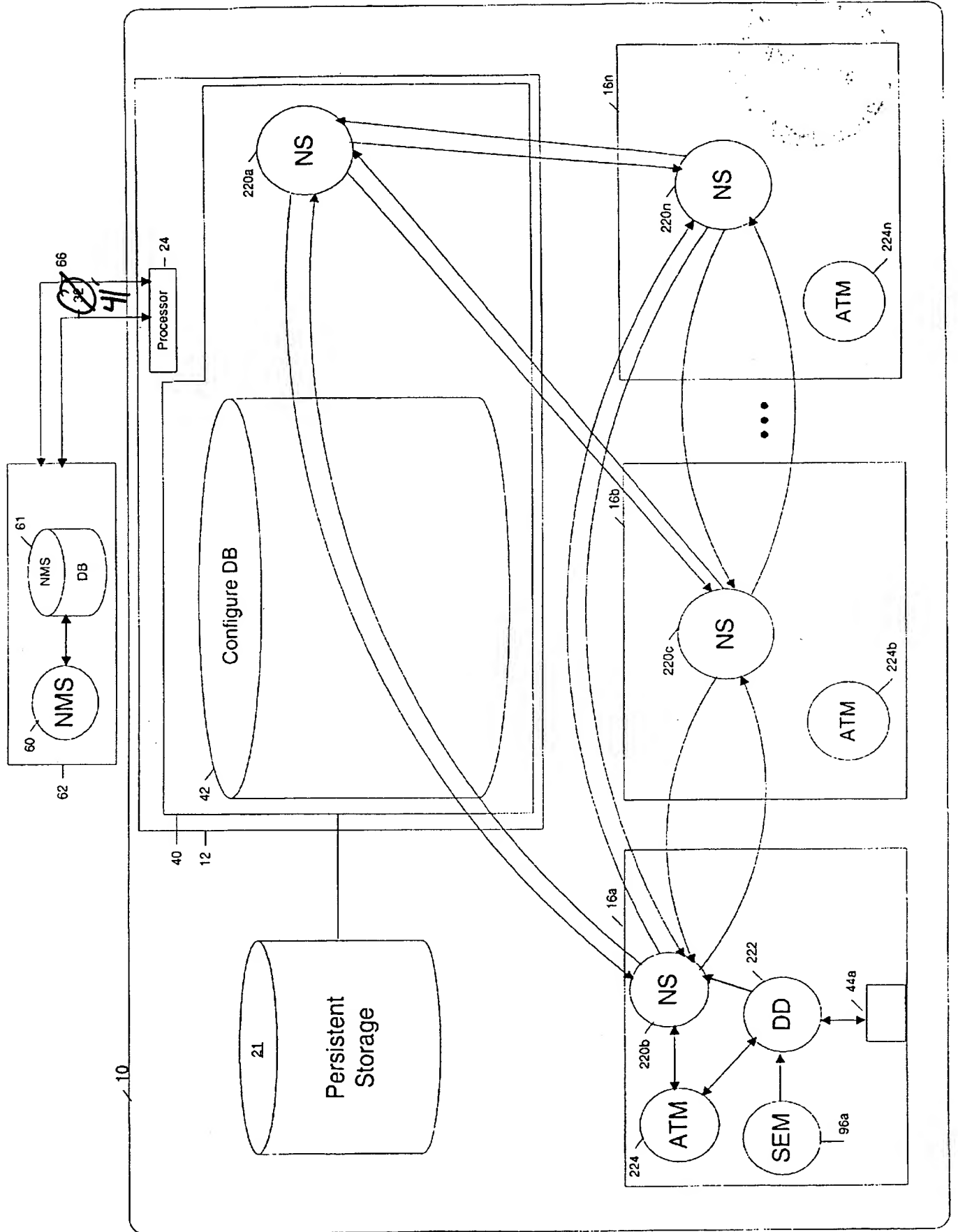


FIG. 17

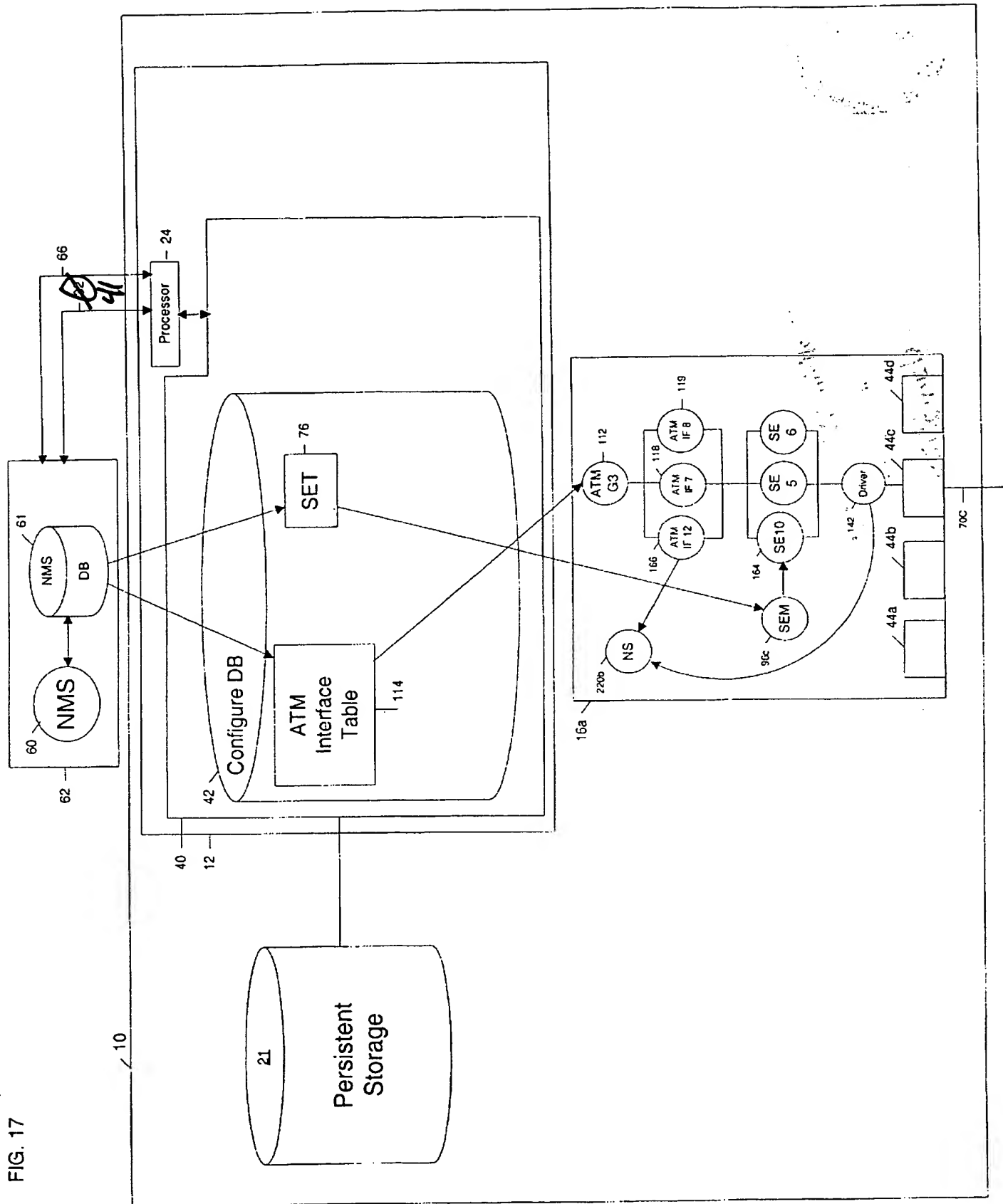
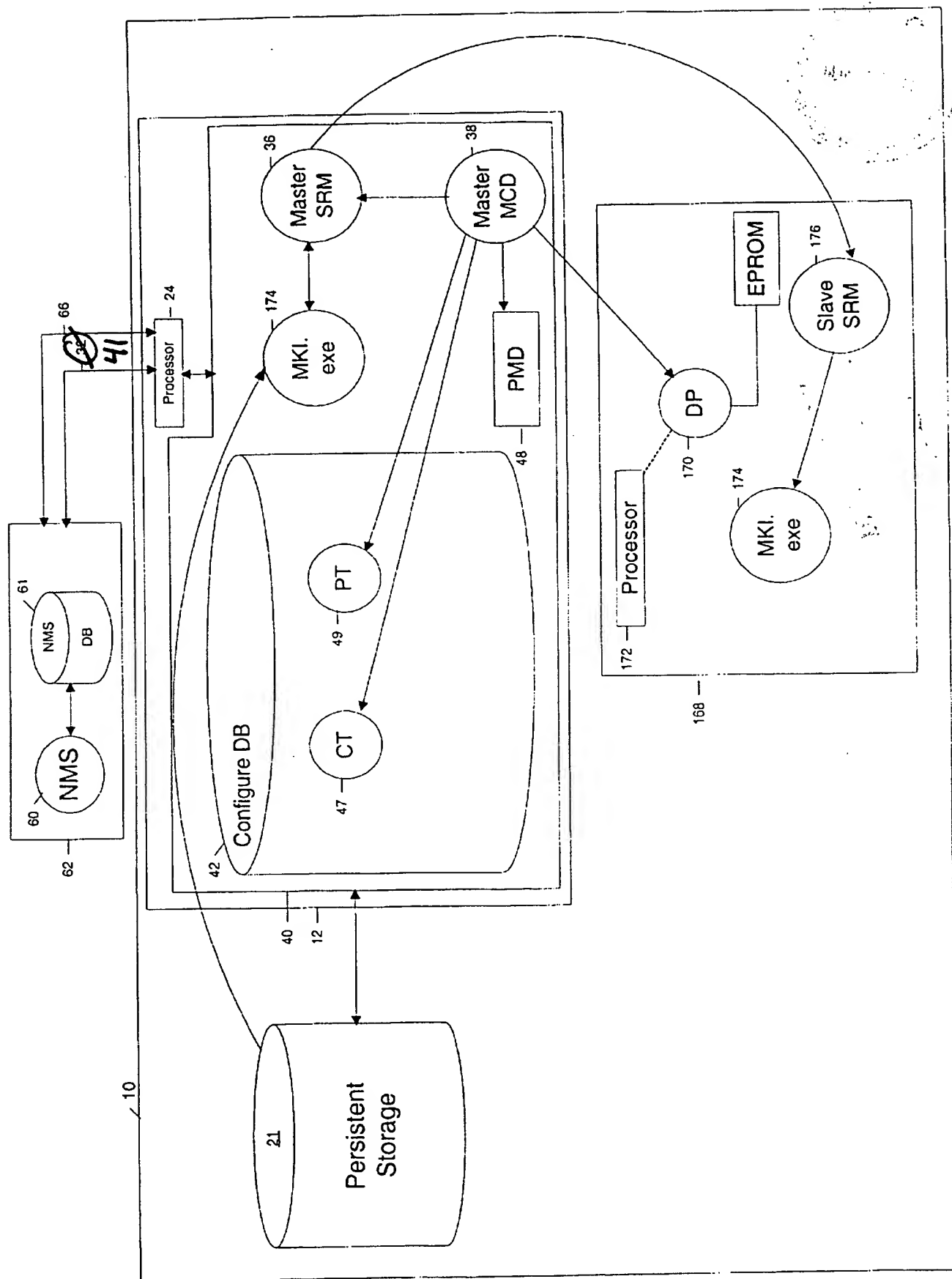


FIG. 18



The diagram illustrates a system architecture with the following components and connections:

- Processor (24)**: Connected to the **NMS DB (61)** and the **Master MCD (38)**.
- Master SRM (36)**: Connected to the **Master MCD (38)**.
- DD exe (180)**: Connected to the **Master MCD (38)** and the **Slave MCD (178)**.
- PMD (48)**: Connected to the **Master MCD (38)** and the **Slave MCD (178)**.
- Slave MCD (178)**: Connected to the **Slave SRM (176)**.
- Slave SRM (176)**: Connected to the **DD exe (180)**.
- DD exe (180)**: Connected to the **Slave MCD (178)**.
- CT (47)**: Connected to the **Configure DB (42)** and the **DD exe (180)**.
- Configure DB (42)**: Connected to the **Processor (24)** and the **CT (47)**.
- NMS DB (61)**: Connected to the **Processor (24)** and the **CT (47)**.
- Persistent Storage (21)**: Connected to the **Configure DB (42)** and the **CT (47)**.

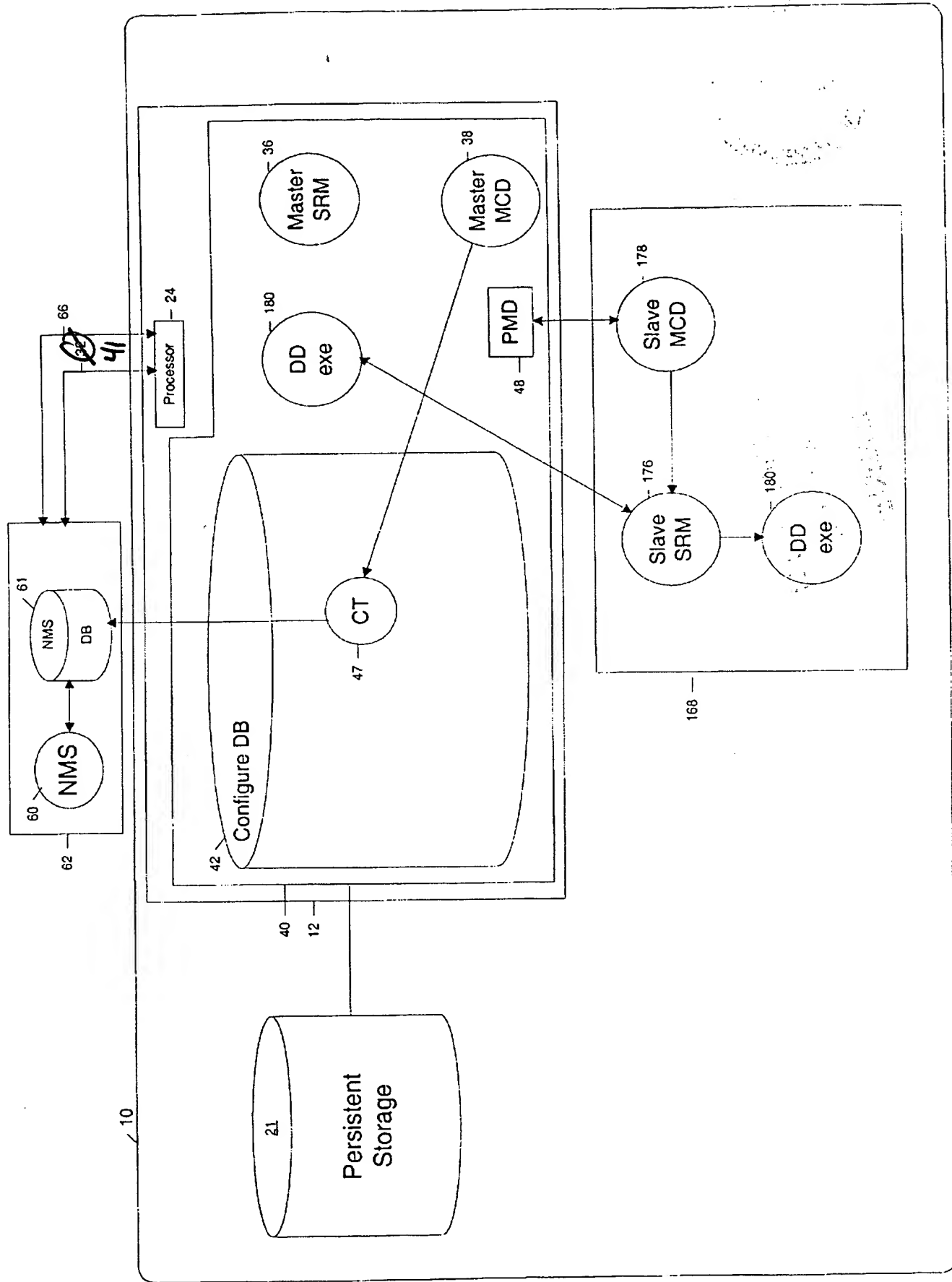


FIG. 20

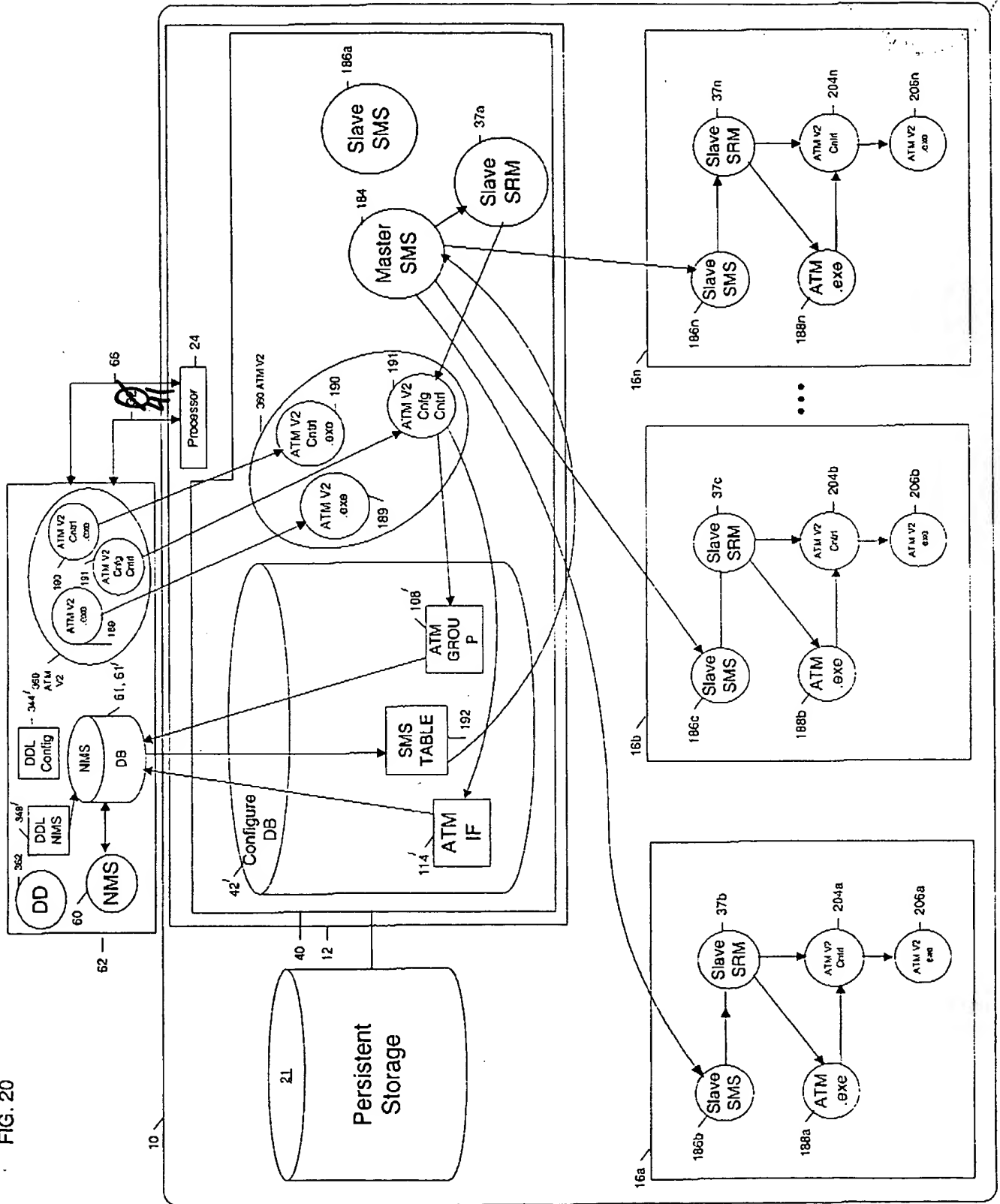


FIG. 23

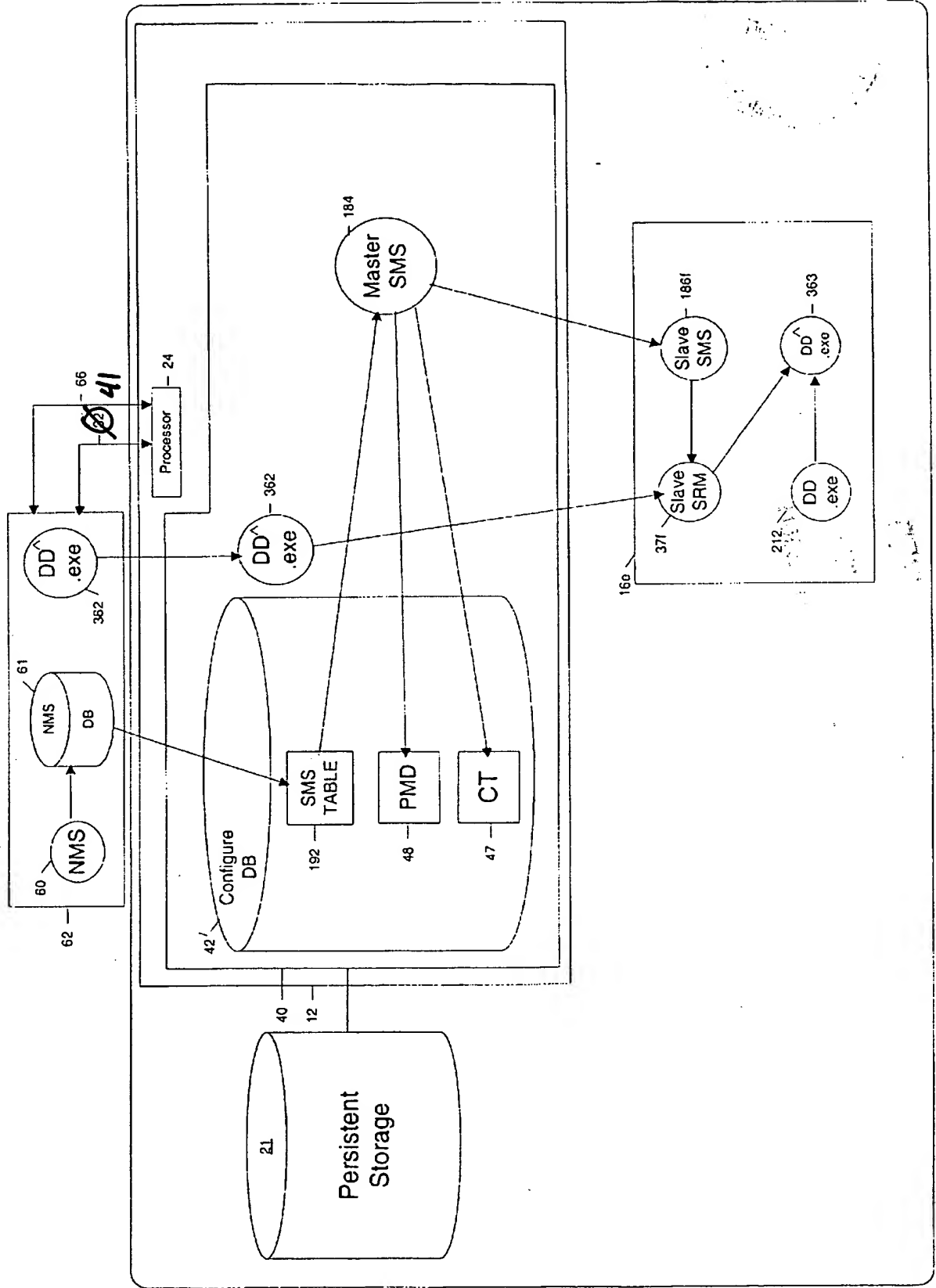


FIG. 26

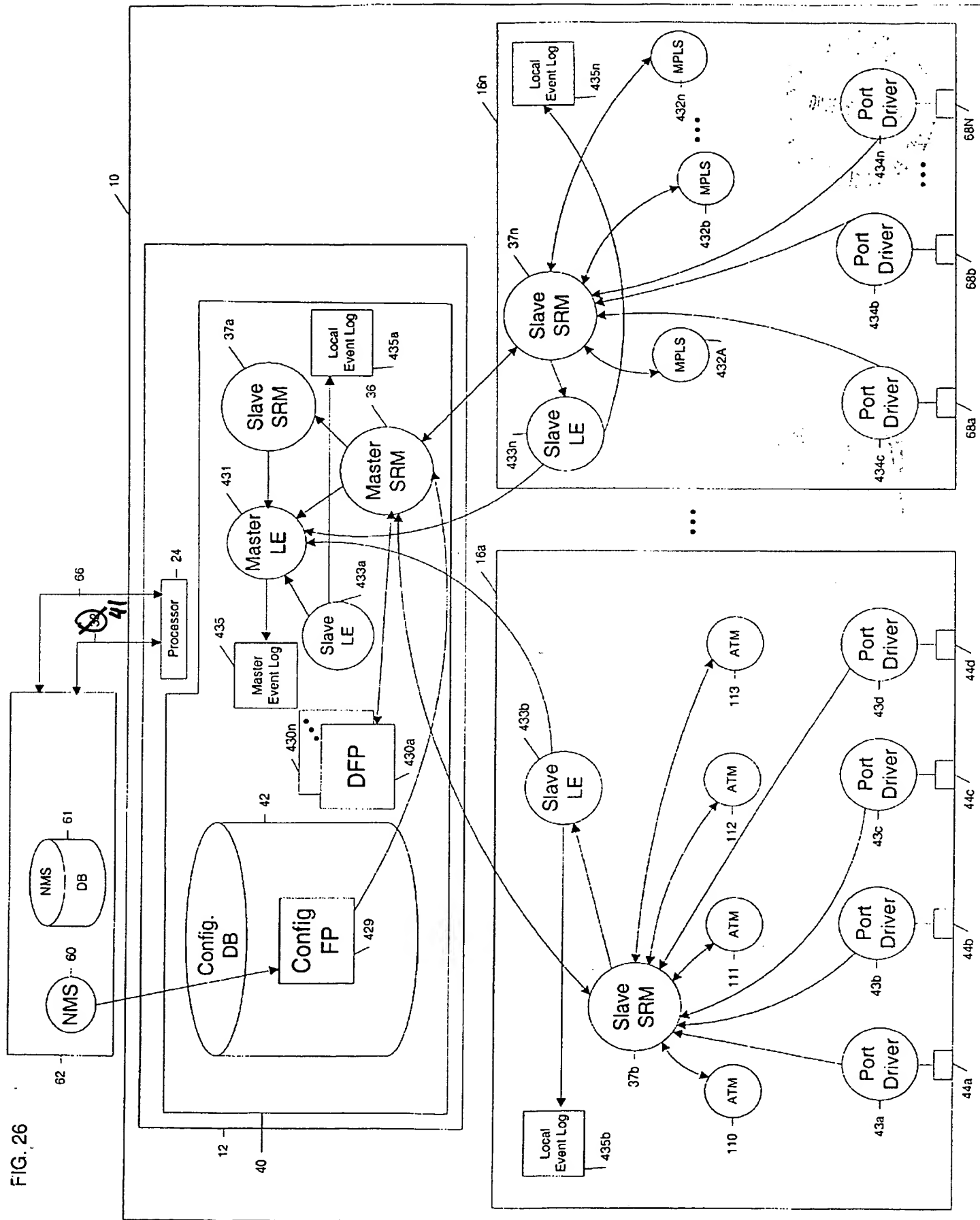


FIG. 35

